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Influence of the Si/SiO₂ interface on the charge carrier density of Si nanowires

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ABSTRACT The electrical properties of Si nanowires covered by a SiO₂ shell are influenced by the properties of the Si/SiO₂ interface. This interface can be characterized by the fixed oxide charge density $Q_{\rm f}$ and the interface trap level density $D_{\rm it}$. We derive expressions for the effective charge carrier density in silicon nanowires as a function of $Q_{\rm f}$, $D_{\rm it}$, the nanowire radius, and the dopant density. It is found that a nanowire is fully depleted when its radius is smaller than a critical radius $a_{\rm crit}$. An analytic expression for $a_{\rm crit}$ is derived.

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1 Introduction

The potential use of silicon nanowires as parts of future devices has triggered an increased interest in silicon nanowire research. The application of silicon nanowires, however, requires well defined electrical properties and this is a subtle problem as surface charges are known to influence the charge carrier density of semiconductors [1]. This is especially problematic for nanowires, which, due to their rod-like geometry and their nanoscale dimensions, have an extremely large surface-to-volume ratio. Consequently, their properties might be strongly influenced by the nanowire surface; an effect which is sometimes considered [2–7] but often neglected [8–10].

Silicon is known to oxidize rapidly in air, thus considering the electrical properties of silicon nanowires, the influence of charges located within the SiO_2 shell and/or at the Si/SiO_2 interface have to be taken into account. It is the scope of this paper to discuss the influence of the Si/SiO_2 interface on the charge carrier density of silicon nanowires. Expressions for the effective charge carrier density of a silicon nanowire as a function of the nanowire radius, the doping concentration and the interface charge density will be derived.

2 Silicon/silicon dioxide interface properties

Silicon nanowires are usually covered by an SiO_2 shell. According to the most commonly used nomenclature [11], one distinguishes between electric charges located

deep inside the SiO_2 shell and charges located at or in close vicinity of the Si/SiO_2 interface. Charges inside the SiO_2 layer are classified as oxide trapped charges or mobile ionic charges with respective densities $Q_{\rm ot}$ and $Q_{\rm m}$.

Charges at or in close vicinity of the Si/SiO2 interface, can also be subdivided into two categories: the fixed oxide charge density $Q_{\rm f}$, and the interface trap charge density $Q_{\rm it}$. In the following discussion we will concentrate on the effect of Q_f and Q_{it} on the charge density of the nanowire, due to their stronger influence on the electric properties [12]. The fixed oxide charges, by definition, do not interact via a charge transfer with the underlying silicon so that Q_f does not depend on the position of the Fermi-level. Electron-spinresonance studies showed that the trapped oxide charges may be partly attributed to so-called E' resonance centers, which are trivalent silicons, bonded to three oxygens [13–16]. Unlike the fixed oxide charges, the interface trap charges do interact via a charge transfer with the Si body of the nanowire. The interface trap charge density Q_{it} arises by charging interface states of level density D_{it} [cm⁻²eV⁻¹]. The nature of these interface states is closely related to the so-called P_b resonance centers, first observed by Nishi et al. [17–19] in electron-spin-resonance studies. These P_b centers could be identified as trivalent silicon atoms [19] bonded to three underlying silicon atoms [20, 21]. Extensive electron-spinresonance studies on silicon/oxide capacitors showed that the $P_{\rm b}$ centers are the main cause for the presence of interface states [16, 20, 22–26], so that the characteristics of the interface traps is determined by the properties of the P_b centers. It was found that the P_b centers are amphoteric [16]. In the lower half of the bandgap a P_b center is donor-like $(P_{\rm b} \to e^- + P_{\rm b}^+)$, which means that, supposing the Fermilevel is located in the lower half of the bandgap, then the $P_{\rm b}$ centers below the Fermi-level are neutral, whereas the P_b centers between the Fermi-level and the bandgap middle are positively charged. The P_b centers in the upper half of the bandgap behave acceptor-like $(P_b + e^- \rightarrow P_b^-)$. If the Fermi-level is located in the upper half of the bandgap, the P_b centers between the Fermi-level and the bandgap middle are negatively charged, while the ones above the Fermi-level are neutral.

In addition to spin-resonance measurements, the interface trap level density was also determined by capacitance [12], and photovoltage measurements [27, 28], deep-level transient spectroscopy [26], measurements of the subthresh-

old slope [29], irradiation and annealing studies [30–32], and simulations [33]. The outcome is that the density of Si/SiO₂ interface trap states is U-shaped, being approximately constant within a certain range of about ± 0.25 eV around the midgap (see [34, 35]). The magnitude of the interface trap level density can vary between approximately $D_{\rm it} = 1 \times 10^{10}$ cm⁻²eV⁻¹ and $D_{\rm it} = 1 \times 10^{12}$ cm⁻²eV⁻¹, depending on the oxidation and annealing conditions.

3 Theory

In the following, a model for the effective charge carrier density in a nanowire as a function of the interface trapped charge density $Q_{\rm it}$ and the fixed oxide charge density $Q_{\rm f}$ will be derived. To simplify the problem, we consider a circular slice of silicon of radius a covered by an oxide layer, which is equivalent to considering an infinitely long homogeneously doped cylindrical nanowire. Edge effects, like the diffusion of charge carriers from the substrate into the nanowire for example or the effect of metal contacts are neglected.

Figure 1 schematically depicts the radial band diagram of a circular slice of n-type silicon in the presence of interface traps of level density $D_{\rm it}$, additionally assuming $Q_{\rm f}=0$. The Fermi-level $E_{\rm F}$ at the surface is located above the midgap level $E_{\rm i}$, which corresponds to a positive surface potential $\psi_{\rm s}$. As discussed before, the interface traps in the upper half of the bandgap between $E_{\rm F}$ and $E_{\rm i}$ will accept one electron each and become negatively charged (see the shaded region in Fig. 1). The necessary electrons are supplied by the n-type nanowire such that a depletion layer, extending from the nanowire surface at r=a to an inner radius $r_{\rm d}$, develops. Within the depletion layer a net positive charge is present.

Defining the electrostatic potential $\psi(r)$ in a way that it equals the midgap level E_i , taking the donors and acceptors of density $N_{\rm D}$ and $N_{\rm A}$ to be fully ionized, and employing the Boltzmann approximation, the charge density $\varrho(r)$ can be written as [34]

$$\varrho(r) = q \left(p_0 \exp(-\beta \psi(r)) - n_0 \exp(\beta \psi(r)) + N_D - N_A \right),$$
(1)

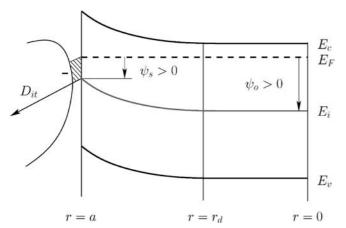


FIGURE 1 Schematic of the Si/SiO₂ interface and the radial band-bending of n-doped nanowire assuming $Q_{\rm f} = 0$. The *shaded area* indicates filled interface trap levels of density $D_{\rm it}$ leading to a negative surface charge and a depletion of the nanowire in the region $r_{\rm d} < r < a$

with $\beta = q/kT$. The parameters n_0 and p_0 in the above equation are given by

$$n_0 = N_c \exp\left(-\frac{E_g}{2kT}\right),\tag{2}$$

$$p_0 = N_{\rm v} \, \exp\left(-\frac{E_{\rm g}}{2kT}\right)\,,\tag{3}$$

where $N_{\rm c}$ and $N_{\rm v}$ are the density of states in the conduction and valence band, respectively. Furthermore, we will adopt the full depletion approximation, i.e. assuming an abrupt transition between the depletion layer and the non-depleted semiconductor material (neglecting charge carrier diffusion between the depleted and the non-depleted region) so that the charge density is approximated as

$$\varrho = \begin{cases}
0 & 0 \le r < r_{\rm d} \\
q(N_{\rm D} - N_{\rm A}) & r_{\rm d} \le r \le a
\end{cases}$$
(4)

By solving the Poisson equation in polar coordinates, the electrostatic potential $\psi(r)$ can be obtained

$$\psi(r) = \begin{cases} \psi_0 & 0 \le r < r_{\rm d} \\ \psi_0 - \frac{\varrho}{4\varepsilon_{\rm s}} \left(r^2 - r_{\rm d}^2 \right) & r_{\rm d} \le r \le a \end{cases}$$
 (5)

According to the above equation, the potential at the silicon surface $\psi_s = \psi(a)$ is

$$\psi_{\rm s} = \psi_0 - \frac{\varrho}{4\varepsilon_{\rm s}} \left(a^2 - r_{\rm d}^2 \right) \,, \tag{6}$$

with ψ_0 being the potential at r = 0. If the nanowire is only depleted at its surface, the charge density ϱ vanishes for $r \le r_d$. The value of the potential ψ_0 can be obtained by setting (1) to zero and solving for $\psi_0 = \psi(0)$ [12]

$$\psi_0 = \beta^{-1} \ln \left\{ \frac{N_{\rm D} - N_{\rm A}}{2n_0} \left(1 + \left(1 - \frac{2n_0 p_0}{(N_{\rm D} - N_{\rm A})^2} \right)^{1/2} \right) \right\} . \tag{7}$$

Let us come back to the $\mathrm{Si/SiO_2}$ interface and its properties. According to the definition, the interface charge density is given by the sum of Q_{it} and Q_{f} , where Q_{f} is constant and independent of the position of the Fermi-level. The interface trapped charges of density Q_{it} are assumed to have the same characteristic as the P_{b} centers, i.e. they are taken to be amphoteric. Furthermore, they are assumed to be neutral if $E_{\mathrm{F}} = E_{\mathrm{i}}$. Especially for high trap level densities D_{it} and low doping concentrations, the charge neutrality condition forces the Fermi-level at the nanowire surface to be located around midgap. Thus in this limit (high D_{it} , low doping) the situation could equally be described by pinning the Fermi-level at the nanowire surface at $E_{\mathrm{F}} = E_{\mathrm{i}}$. Approximating the Fermi distribution by a step function and assuming D_{it} to be constant around midgap, Q_{it} can be expressed as [28]

$$Q_{\rm it} = -q^2 D_{\rm it} \psi_{\rm s} \,. \tag{8}$$

Charge neutrality provides another condition. Demanding that the nanowire slice is neutral, the space charge in the depletion layer has to equal the interface charge, which gives

$$\pi \left(a^2 - r_{\rm d}^2 \right) \varrho + 2\pi a (Q_{\rm f} + Q_{\rm it}) = 0.$$
 (9)

Using (6) and (8) and solving for the depletion radius r_d leads to

$$r_{\rm d} = \sqrt{a^2 + \frac{2aQ_{\rm f} - 2aq^2D_{\rm it}\psi_0}{\varrho\left(1 + \frac{aq^2}{2\varepsilon_{\rm s}}D_{\rm it}\right)}}.$$
 (10)

One can see that for a specific set of parameters $Q_{\rm f}$, $D_{\rm it}$, a, and ψ_0 the inner radius of the depletion layer can become zero. This defines a critical nanowire radius

$$a_{\text{crit}} = \frac{\varepsilon_{\text{s}}}{q^2 D_{\text{it}}} \left\{ -1 + \left[1 + \frac{4q^2 D_{\text{it}}}{\varrho \varepsilon_{\text{s}}} \left(q^2 D_{\text{it}} \psi_0 - Q_{\text{f}} \right) \right]^{1/2} \right\}. \tag{11}$$

If the nanowire radius a is larger than a_{crit} , we face the situation that the nanowire is depleted at its surface, but still conductive in its center, see Fig. 1. If, however, the nanowire radius a is smaller than a_{crit} the nanowire is fully depleted. This is consistent with the observation of Calarco et al. for GaN nanowires [2]. Figure 2 shows the critical radius of an n-doped nanowire for different interface trap level densities D_{it} and zero fixed oxide charges $(Q_f = 0)$ as a function of the donor concentration N_D . The critical radius a_{crit} decreases with increasing doping concentration and decreasing $D_{\rm it}$. This could be expected, since both, an increase of the doping, and a reduction of the interface trap level density reduces the width of the depletion layer. We can deduce from Fig. 2 that the doping of a silicon nanowire of 50 nm in diameter, assuming a medium trap level density of $D_{it} = 1 \times 10^{11} \text{ eV}^{-1} \text{cm}^{-2}$ and zero $Q_{\rm f}$, has to be larger than $3 \times 10^{16} \, {\rm cm}^{-3}$ to prevent a full depletion of the nanowire. By expanding the square root of (11) to the first order we can obtain an approximated expression for a_{crit}

$$a_{\rm crit} \approx \frac{2}{\varrho} \left(q^2 D_{\rm it} \psi_0 - Q_{\rm f} \right) \,, \tag{12}$$

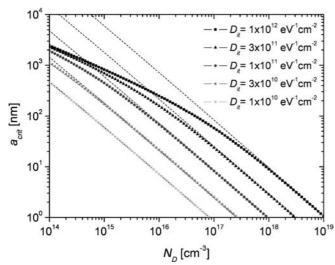


FIGURE 2 Critical radius $a_{\rm crit}$ (according to (11)) as a function of the donor concentration $N_{\rm D}$ for different interface trap level densities $D_{\rm it}$, assuming $Q_{\rm f}=0$; dashed lines correspond to the approximated values for $a_{\rm crit}$ according to (12)

where ϱ is $q(N_{\rm D}-N_{\rm A})$ and ψ_0 has to be determined by using (7). This formula seems sufficiently accurate for most practical cases. The obtained approximated values of $a_{\rm crit}$ are displayed in Fig. 1 as dotted lines.

Let us concentrate now on the influence of the interface charges on the charge carrier density. Depending on whether the radius is greater or smaller $a_{\rm crit}$, we have to distinguish two cases. The first case corresponds to $a > a_{\rm crit}$. Considering an n-doped nanowire, we can evaluate the effective electron concentration $n_{\rm eff}$ (the greater sign indicates that $a > a_{\rm crit}$) by combining (2) and (5) and averaging over the nanowire area

$$n_{\text{eff}>} = \frac{1}{a^{2}\pi} \int_{0}^{a} n_{0} \exp(\beta \psi(r)) 2\pi r dr$$

$$= \frac{2n_{0}}{a^{2}} \left\{ \int_{0}^{r_{d}} \exp(\beta \psi_{0}) r dr$$

$$+ \int_{r_{d}}^{a} \exp\left(\beta \psi_{0} - \beta \frac{\varrho}{4\varepsilon_{s}} \left(r^{2} - r_{d}^{2}\right)\right) r dr \right\}$$

$$= n_{0} \exp(\beta \psi_{0}) \left\{ \frac{r_{d}^{2}}{a^{2}} + \frac{4\varepsilon_{s}}{\beta \varrho a^{2}} \left[1 - \exp\left(\beta \frac{\varrho}{4\varepsilon_{s}} \left(r_{d}^{2} - a^{2}\right)\right) \right] \right\}, \quad (13)$$

with ψ_0 given by (7).

The second case corresponds to a fully depleted wire ($a < a_{crit}$). The electrostatic potential can then be expressed as

$$\psi(r) = \psi_s + \frac{\varrho}{4\varepsilon_s} (a^2 - r^2), \qquad (14)$$

where the surface potential is determined by the charge neutrality condition

$$\psi_{\rm s} = \frac{a\varrho + 2Q_{\rm f}}{2q^2 D_{\rm it}} \,. \tag{15}$$

The effective electron density $n_{\rm eff<}$ for the fully depleted case $(a < a_{\rm crit})$ can be obtained by averaging the electron density over the nanowire area

$$n_{\text{eff}<} = \frac{1}{a^2 \pi} \int_0^a n_0 \exp(\beta \psi(r)) 2\pi r dr$$

$$= n_0 \exp(\beta \psi_s) \frac{4\varepsilon_s}{\beta \varrho a^2} \left[\exp\left(\beta \frac{\varrho a^2}{4\varepsilon_s}\right) - 1 \right]. \tag{16}$$

Combining (13) and (16), we can evaluate the effective electron density $n_{\rm eff}$ for an arbitrary combination of $N_{\rm D}$, $Q_{\rm f}$ and $D_{\rm it}$. Figure 3 shows $n_{\rm eff}$ for different interface trap level densities $D_{\rm it}$, zero $Q_{\rm f}$, and a radius a of 25 nm as a function of the donor density $N_{\rm D}$. The characteristic feature of the curves is a sharp transition between a region of high and a region of low effective electron density. This transition is located at the $N_{\rm D}$ value, where the wire starts to be fully depleted. Similarly to Fig. 2, the onset of full depletion is shifted on the $N_{\rm D}$ axis depending on the density of interface trap levels. The smaller the

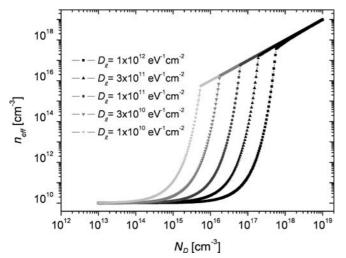


FIGURE 3 Effective electron densities $n_{\rm eff}$ for different values of $D_{\rm it}$, assuming $Q_{\rm f}=0$ and a=25 nm

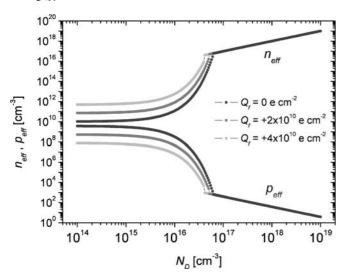


FIGURE 4 Effective electron and hole density, $n_{\rm eff}$ and $p_{\rm eff}$, as a function of $N_{\rm D}$ for a=25 nm, $D_{\rm it}=1\times10^{11}~{\rm eV}^{-1}{\rm cm}^{-2}$ and different values of $Q_{\rm f}\geq0$

 $D_{\rm it}$ value is, the smaller the doping concentration can be, without causing a full depletion of the nanowire. Since the fixed oxide charge was set to zero, the effective electron concentration approaches the intrinsic value of $1.5 \times 10^{10} \, {\rm cm}^{-3}$ in the limit of low doping concentrations.

In order to demonstrate the effect of positive fixed oxide charges, Fig. 4 shows $n_{\rm eff}$ and $p_{\rm eff}$ as a function of $N_{\rm D}$, assuming a medium interface trap level density ($D_{\rm it} = 1 \times 10^{11} \, {\rm eV^{-1} cm^{-2}}$) and a nanowire radius of 25 nm. In the low $N_{\rm D}$ region, where the nanowire is fully depleted, the effective electron density is basically determined by the position of the surface potential. Equation (15) implies that a positive $Q_{\rm f}$ increases the surface potential and, concomitantly, the electron concentration. This can be seen in Fig. 4: in the limit of low doping concentrations, $n_{\rm eff}$ is greater than the intrinsic electron concentration.

Considering negative fixed oxide charges, the effect is reversed. As depicted in Fig. 5, a negative fixed oxide charge density, $Q_{\rm f}$, leads for small $N_{\rm D}$ -values to an effective electron concentration, $n_{\rm eff}$, that is smaller and an effective hole

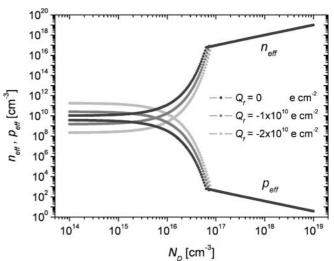


FIGURE 5 $n_{\rm eff}$ and $p_{\rm eff}$ as a function of $N_{\rm D}$ for $a=25\,{\rm nm},~D_{\rm it}=1\times10^{11}\,{\rm eV^{-1}cm^{-2}}$ and different values of $Q_{\rm f}\leq0$

density, $p_{\rm eff}$, that is greater than the intrinsic one. Thus the nanowire is slightly inverted.

4 Conclusion

To conclude, we have seen that, especially for nanowires of small radius, the interface trap level density $D_{\rm it}$ has a considerable influence on the effective charge carrier density. Depending on the interface trap level density, the doping concentration, and the nanowire radius a, the effective charge carrier density might be substantially lower than the doping concentration. We have derived an expression for a critical nanowire radius $a_{\rm crit}$, which marks the border between nanowires that are fully depleted ($a < a_{\rm crit}$) and nanowires which are only depleted at their surface ($a > a_{\rm crit}$).

REFERENCES

- 1 W. Shockley, G.L. Pearson, Phys. Rev. 74, 232 (1948)
- 2 R. Calarco, M. Marso, T. Richter, A.I. Aykanat, R. Meijers, A. v. d. Hart, T. Stoica, H. Lüth, Nano Lett. 5, 981 (2005)
- 3 K. Seo, S. Sharma, A.A. Yasseri, D.R. Stewart, T.I. Kamins, Electrochem. Solid State Lett. 9, G69 (2006)
- 4 T. Stoica, R.J. Meijers, R. Calarco, T. Richter, E. Sutter, H. Lüth, Nano Lett. 6, 1541 (2006)
- 5 I. Kimukin, M.S. Islam, R.S. Williams, Nanotechnology 17, S240 (2006)
- 6 M.H.M. van Weert, O. Wunnike, A.L. Roest, T.J. Eijkemans, A.Y. Silov, J.E.M. Haverkort, G.W. 't Hooft, E.P.A.M. Bakkers, Appl. Phys. Lett. 88, 043 109 (2006)
- 7 V. Schmidt, S. Senz, U. Gösele, Nano Lett. 5, 931 (2005)
- 8 Y. Cui, X. Duan, J. Hu, C.M. Lieber, J. Phys. Chem. B 104, 5213 (2000)
- 9 G. Zheng, W. Lu, S. Jin, C.M. Lieber, Adv. Mater. 16, 1890 (2004)
- 10 C. Yang, Z. Zhong, C.M. Lieber, Science 310, 1304 (2005)
- 11 B.E. Deal, IEEE Trans. Electron. Dev. **27**, 606 (1980)
- 12 E.H. Nicollian, J.R. Brews, MOS (Metal Oxide Semiconductor) Physics and Technology (Wiley, New York, 1982)
- 13 R.H. Silsbee, J. Appl. Phys. 32, 1459 (1961)
- 14 R.A. Weeks, J. Appl. Phys. 27, 1376 (1956)
- 15 D.L. Griscom, Phys. Rev. B 22, 4192 (1980)
- 16 P.M. Lenahan, P.V. Dressendorfer, J. Appl. Phys. **55**, 3495 (1984)
- 17 Y. Nishi, Japan. J. Appl. Phys. **5**, 333 (1966)
- 18 Y. Nishi, Japan. J. Appl. Phys. 10, 52 (1971)
- 19 Y. Nishi, K. Tanaka, A. Ohwada, Japan. J. Appl. Phys. 11, 85 (1972)
- P.J. Caplan, E.H. Poindexter, B.E. Deal, R.R. Razouk, J. Appl. Phys. 50, 5847 (1979)

- 21 E.H. Poindexter, P.J. Caplan, B.E. Deal, R.R. Razouk, J. Appl. Phys. 52, 879 (1981)
- 22 P.M. Lenahan, P.V. Dressendorfer, Appl. Phys. Lett. **41**, 542 (1982)
- 23 P.M. Lenahan, P.V. Dressendorfer, J. Appl. Phys. **54**, 1457 (1983)
- 24 P.M. Lenahan, P.V. Dressendorfer, Appl. Phys. Lett. 44, 96 (1984)
- 25 Y.Y. Kim, P.M. Lenahan, J. Appl. Phys. 64, 3551 (1988)
- 26 N.M. Johnson, D.K. Biegelsen, M.D. Moyer, S.T. Chang, E.H. Poindexter, P.J. Caplan, Appl. Phys. Lett. 43, 563 (1983)
- 27 C. Munakata, S. Nishimatsu, N. Honma, K. Yagi, Japan. J. Appl. Phys. 23, 1451 (1984)
- 28 C. Munakata, S. Nishimatsu, Japan. J. Appl. Phys. 25, 807 (1986)

- 29 J.S. Lyo, K.S. Nam, C. Lee, Japan. J. Appl. Phys. 32, 4393 (1993)
- 30 P.J. McWhorter, P.S. Winokur, Appl. Phys. Lett. 48, 133 (1986)
- 31 D.M. Fleetwood, Appl. Phys. Lett. 60, 2883 (1992)
- 32 D.M. Fleetwood, P.S. Winokur, R.A. Reber, T.L. Meisenheimer, J.R. Schwank, M.R. Shaneyfelt, L.C. Riewe, J. Appl. Phys. 73, 5058 (1993)
- R.B. Lauglin, J.D. Joannopoulos, D.J. Chadi, Phys. Rev. B 21, 5733 (1980)
- 34 S.M. Sze, Physics of Semiconductor Devices (Wiley, New York, 1981), p. 385
- 35 M.H. White, J.R. Cricchi, IEEE Trans. Electron. Dev. 19, 1280 (1972)